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DocuColor 7000/7000AP/7002 DocuColor 8000/8000AP/8080 Digital Color Presses

Statement of Volatility

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NOTICE

This document describes the locations, capacities and contents of volatile and non-volatile memory devices within the DocuColor 7000, 7000AP, 7002, 8000, 8000AP, 8002 and 8080 Digital Presses.

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Evaluation and summary of this equipment was completed by:

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June 28, 2011_____ (Date)

Overview

The Xerox DocuColor 7000/7000AP/7002/8000/8000AP/8002 Digital Color Presses contain both volatile and non-volatile semiconductor memory. The memory is located on the following printed wiring board assemblies (PWBA's):

- IOT CPU Memory/Control PWBA
- Customer Replaceable Unit Monitor Controller PWBA
- DRUM Controller PWBA
- BELT Controller PWBA
- First Feeder Module Controller PWBA
- Second Feeder Module Controller PWBA
- SYS Non-volatile Memory PWBA
- SYS Controller PWBA
- Halftone PWBA
- User Interface Controller PWBA

The Xerox DocuColor 7000/7000AP/7002/8000/8000AP/8002/8080 Digital Color Presses contain no magnetic memory devices such as hard disk drives, floppy disk drives or ZIP drives

The Xerox DocuColor 7000/7000AP/7002/8000/8000AP/8002/8080 Digital Color Presses contain no optical memory devices such as CD/CDR/DVD drives.

Notes

In the following tables, the reference numbers in the "Content Notes" and "Volatility Notes" columns refer to the following:

Content Notes:

1. Contains executable system software (OS, Boot Code, Application code, Program constant data, Fonts).
2. Contains No user or job specific data
3. Contains Machine specific data (hardware identification information, system settings, real-time control parameters, print job control state information, performance log information, usage counters).
4. Contains Machine specific data (System admin password, Auditron admin password, Auditron user names, passwords, user preferences)
5. Temporarily contains Machine specific data for real time control of the print engine. May temporarily contain non-image job specific data (job name, statistics – size, number of pages, finishing, etc).
6. Temporarily contains small quantities of raster image data (less than 128 scan lines) for current or most recently printed page.

Volatility Notes:

7. All content is destroyed when system power is removed for at least 20 seconds (using white switch on top of unit or red switch behind front door panel or circuit breaker on rear panel by power cable entrance)
8. Content can be initialized to factory default values using Service Diagnostic software
9. Content is erased and reloaded when system software is upgraded or reinstalled
10. Contents cannot be modified in the field
11. Board is replaceable

All capacities and memory types are based on the latest RoHS compliant PWBA designs. In earlier systems, capacities may be reduced and some Flash memory may be replaced by one-time programmable EPROM.

IOT CPU Memory/Control PWBA

Memory	Type	Capacity	Content Notes	Volatility Notes
ROM embedded in CPU I.C.	Non-Volatile	256Kbytes x 2	1,2	9
Flash ROM	Non-Volatile	4Kbytes x 2	1,2	9
EPROM	Non-Volatile	N/A		
Battery backed up RAM	Non-Volatile	128Kbytes x 4	3,2	8
RAM embedded in CPU I.C.	Volatile	4Kbytes x 2	5,2	7
Static RAM	Volatile	512Kbytes x 4	5,2	7
Dynamic RAM	Volatile	4Mbytes x 4	5,2	7
Registers/FIFOs embedded in ASICS	Volatile	N/A		

Customer Replaceable Unit Monitor Controller PWBA

Memory	Type	Capacity	Content Notes	Volatility Notes
ROM embedded in CPU I.C.	Non-Volatile	256KBytes	1,2	9
Flash ROM	Non-Volatile	N/A		
EPROM	Non-Volatile	N/A		
Battery backed up RAM	Non-Volatile	N/A		
RAM embedded in CPU I.C.	Volatile	8KBytes	5,2	7
Static RAM	Volatile	256Kbytes	5,2	7
Dynamic RAM	Volatile	N/A		
Registers/FIFOs embedded in ASICS	Volatile	N/A		

DRUM Controller PWBA

Memory	Type	Capacity	Content Notes	Volatility Notes
ROM embedded in CPU I.C.	Non-Volatile	N/A		
Flash ROM	Non-Volatile	1MByte x 2	1,2	9
EPROM	Non-Volatile	N/A		
Battery backed up RAM	Non-Volatile	N/A		
RAM embedded in CPU I.C.	Volatile	4Kbytes x 2	5,2	7
Static RAM	Volatile	128K x 8bit x 2	5,2	7
Dynamic RAM	Volatile	N/A		
Registers/FIFOs embedded in ASICS	Volatile	N/A		

BELT Controller PWBA

Memory	Type	Capacity	Content Notes	Volatility Notes
ROM embedded in CPU I.C.	Non-Volatile	N/A		
Flash ROM	Non-Volatile	1MByte	1,2	10
EPROM	Non-Volatile	N/A		
Battery backed up RAM	Non-Volatile	128KBytes	3,2	8,11
RAM embedded in CPU I.C.	Volatile	4KBytes	5,2	7
Static RAM	Volatile	128KBytes	5,2	7
Dynamic RAM	Volatile	N/A		
Registers/FIFOs embedded in ASICS	Volatile	N/A		

Hyper-Registration Controller PWBA

Memory	Type	Capacity	Content Notes	Volatility Notes
ROM embedded in CPU I.C.	Non-Volatile	N/A		
Flash ROM	Non-Volatile	1MByte	1,2	10
EPROM	Non-Volatile	N/A		
Battery backed up RAM	Non-Volatile	N/A		
RAM embedded in CPU I.C.	Volatile	4KBytes	5,2	7
Static RAM	Volatile	128Kbytes x 2	5,2	7
Dynamic RAM	Volatile	N/A		
Registers/FIFOs embedded in ASICS	Volatile	N/A		

First Feeder Module Controller PWBA

Memory	Type	Capacity	Content Notes	Volatility Notes
ROM embedded in CPU I.C.	Non-Volatile	256KBytes	1,2	9
Flash ROM	Non-Volatile	N/A		
EPROM	Non-Volatile	N/A		
Battery backed up RAM	Non-Volatile	N/A		
RAM embedded in CPU I.C.	Volatile	4KBytes	5,2	7
Static RAM	Volatile	128KBytes	5,2	7
Dynamic RAM	Volatile	N/A		
Registers/FIFOs embedded in ASICS	Volatile	N/A		

Second Feeder Module Controller PWBA

Memory	Type	Capacity	Content Notes	Volatility Notes
ROM embedded in CPU I.C.	Non-Volatile	256KBytes	1,2	9
Flash ROM	Non-Volatile	N/A		
EPROM	Non-Volatile	N/A		
Battery backed up RAM	Non-Volatile	N/A		
RAM embedded in CPU I.C.	Volatile	4KBytes	5,2	7
Static RAM	Volatile	128KBytes	5,2	7
Dynamic RAM	Volatile	N/A		
Registers/FIFOs embedded in ASICS	Volatile	N/A		

SYS Non-volatile Memory PWBA

Memory	Type	Capacity	Content Notes	Volatility Notes
ROM embedded in CPU I.C.	Non-Volatile	N/A		
Flash ROM	Non-Volatile	N/A		
EPROM	Non-Volatile	N/A		
Battery backed up RAM	Non-Volatile	512Kbytes x 8	3,2	8,11
RAM embedded in CPU I.C.	Volatile	N/A		
Static RAM	Volatile	N/A		
Dynamic RAM	Volatile	N/A		
Registers/FIFOs embedded in ASICS	Volatile	N/A		

SYS Controller PWBA

Memory	Type	Capacity	Content Notes	Volatility Notes
ROM embedded in CPU I.C.	Non-Volatile	8KBytes	1,2	10
Flash ROM	Non-Volatile	2Mbytes x 2	1,2	9
EPROM	Non-Volatile	N/A		
Battery backed up RAM	Non-Volatile	N/A		
RAM embedded in CPU I.C.	Volatile	4Kbytes + 240Bytes	5	7
Static RAM	Volatile	N/A		
Dynamic RAM	Volatile	2Mbytes x 4	5	7
Registers/FIFOs embedded in ASICS	Volatile	N/A		

Halftone PWBA

Memory	Type	Capacity	Content Notes	Volatility Notes
ROM embedded in CPU I.C.	Non-Volatile	N/A		
Flash ROM	Non-Volatile	N/A		
EPROM	Non-Volatile	1Mbit	2,3	10
Battery backed up RAM	Non-Volatile	N/A		
RAM embedded in CPU I.C.	Volatile	N/A		
Static RAM	Volatile	16k×36bit×4	6	7
Dynamic RAM	Volatile	N/A		
Registers/FIFOs embedded in ASICS	Volatile	10Mbit×4	5,6	7

User Interface Controller PWBA

Memory	Type	Capacity	Content Notes	Volatility Notes
ROM embedded in CPU I.C.	Non-Volatile	N/A		
Flash ROM	Non-Volatile	2Mbytes x 4	1,2	9
EPROM	Non-Volatile	N/A		
Battery backed up RAM	Non-Volatile	32Kbytes x 2	2,4	8
RAM embedded in CPU I.C.	Volatile	N/A		
Static RAM	Volatile	N/A		
Dynamic RAM	Volatile	2Mbytes x 5	5	7
Registers/FIFOs embedded in ASICS	Volatile	N/A		